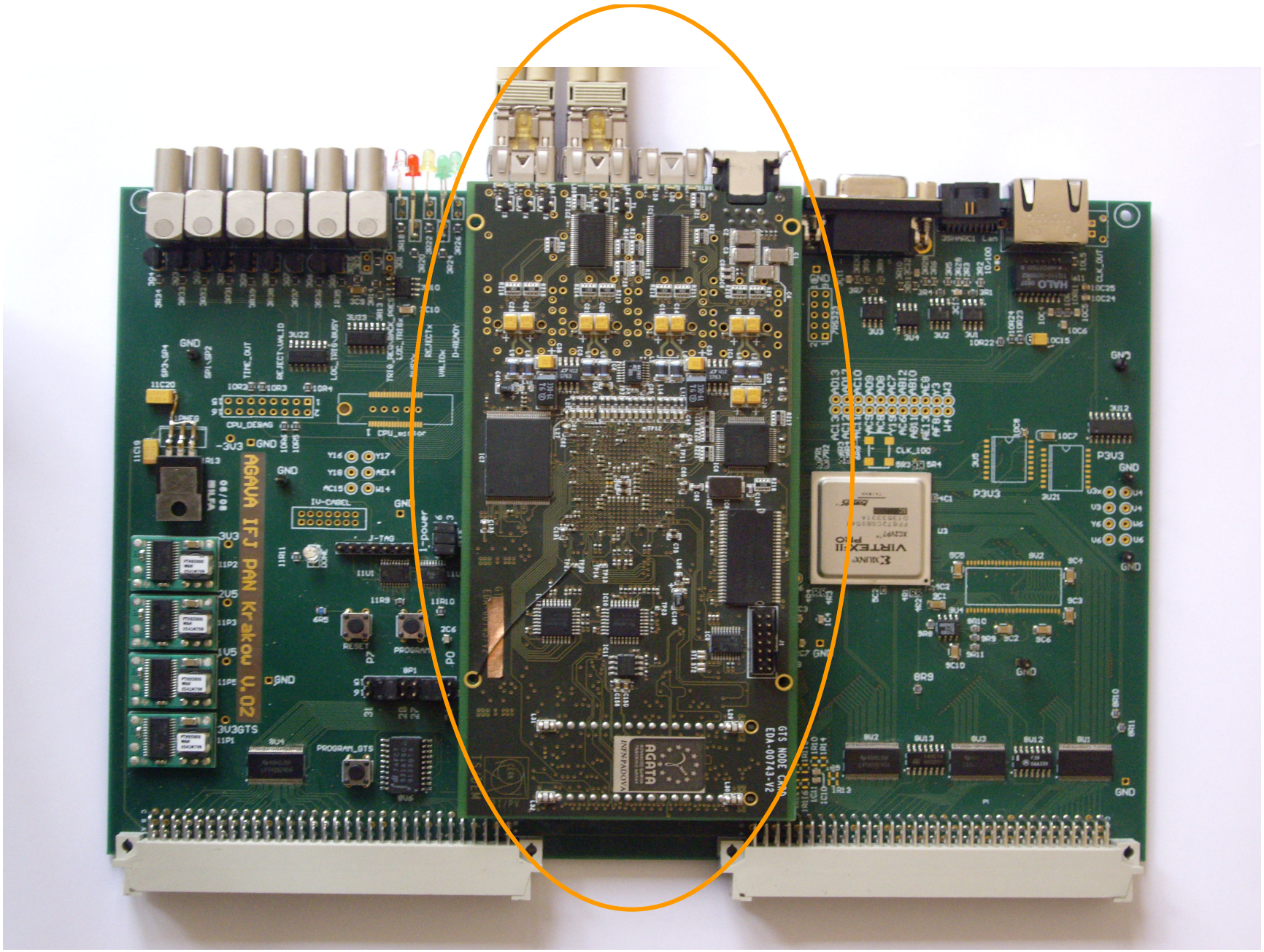
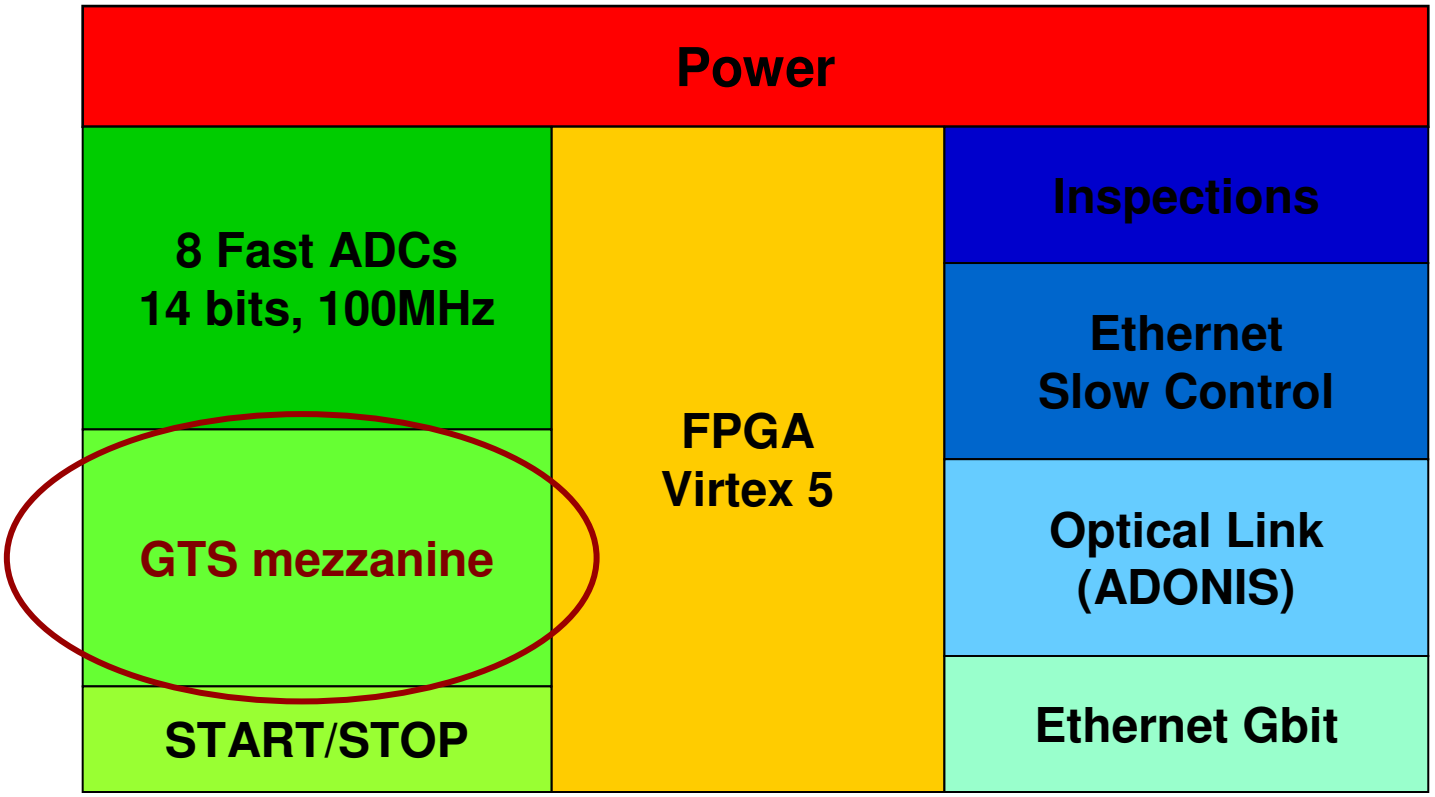
AGAVA Description

- AGAVA Interface is a 1-unit wide A32D32 type VME/VXI slave module. It is also the carrier board for the GTS (Global Trigger and Synchronization) mezzanine card used in the AGATA experiment for the global clock and time stamp distribution.
- The main task of the AGAVA is to merge the triggerless time stamp-based system with an acquisition system using trigger, based on the VME or VXI Exogam-like environment.
- It can also connect systems based on the triggers with the VME Metronome and Shark_link systems.
- The logic and tasks are controlled by the FPGA Virtex II Pro.

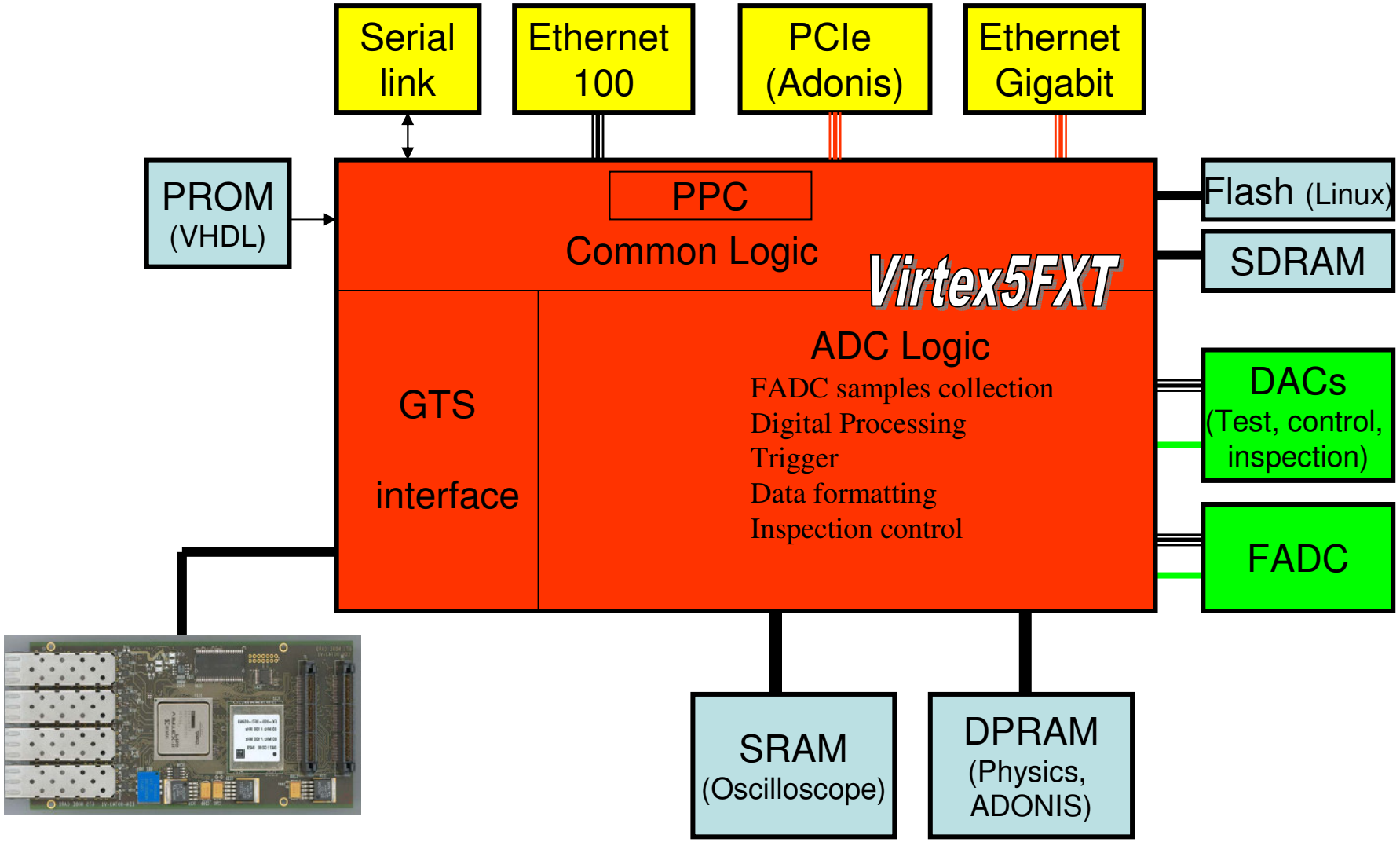

AGAVA IFJ PAN Krakow V.02
GTS NODE CARD
EDA-00743-V2
AGATA
VIRTEX II PRO
P3V3
GND

Block Diagram of NUMEXO2



A) GTS fanin mezzanine implemented on NUMEXO2 carrier

NUMEXO2 block diagram



B) GTS functions embedded in the Virtex 5 of NUMEXO2